

	Type	Hits	Search Text	DBs	Time Stamp
D	1	BRS 1	((((((separate or divide or dividing or separating) adj (channel or soi))) and pixel and tft) and (@ad<20031113 or @	USPAT, US-PGPUB	2004/06/19
P	2	BRS 23	(((separate or divide or dividing or separating) adj (channel or soi)) with capacitor)	USPAT, US-PGPUB	2004/06/19
A	3	BRS 0	(((separate or divide or dividing or separating) adj (channel or soi)) with capacitor) and pixel and tft	USPAT, US-PGPUB	2004/06/19
F	4	BRS 0	(((separate or divide or dividing or separating) adj (channel or soi)) with capacitor) and tft	USPAT, US-PGPUB	2004/06/19
S	5	BRS 23	(((separate or divide or dividing or separating) adj (channel or soi)) with capacitor) and (@ad<20031113 or @rlad	USPAT, US-PGPUB	2004/06/19
T	6	BRS 0	(((separate or divide or dividing or separating) near (channel or soi or silicon or body)) with capacitor) and pixel an	USPAT, US-PGPUB	2004/06/19
U	7	BRS 0	(((separate or divide or dividing or separating) near (channel or soi or silicon or body)) with capacitor) and tft	USPAT, US-PGPUB	2004/06/19
Q	8	BRS 16	((((((channel or soi or silicon or body or substrate) with capacitor) and pixel and tft) and (@ad<20031113 or @rla	USPAT, US-PGPUB	2004/06/19
T	9	BRS 0	((((((channel or soi or silicon or body or substrate) with capacitor) and pixel and tft) and (@ad<20031113 or @rlad	USPAT, US-PGPUB	2004/06/19
	10	BRS 0	((((((channel or soi or silicon or body or substrate) with capacitor) and pixel and tft) and (@ad<20031113 or @rlad	USPAT, US-PGPUB	2004/06/19
	11	BRS 24	((((((channel or soi or silicon or body or substrate) with capacitor) and pixel and tft) and (@ad<20031113 or @rlad	USPAT, US-PGPUB	2004/06/19
	12	BRS 3	((((((channel or soi or silicon or body or substrate) with capacitor) and pixel and tft) and (@ad<20031113 or @rlad	USPAT, US-PGPUB	2004/06/19
	13	BRS 1	"5317432".PN	USPAT	2004/06/19
	14	BRS 1	"5717224".PN	USPAT	2004/06/19
	15	BRS 2	"05061071"	DERWENT	2004/06/19
	16	BRS 4	"05061071"	JPO, DERWENT	2004/06/19
	17	BRS 15	((((((channel or soi or silicon or body or substrate) with capacitor) and pixel and tft) and (@ad<20031113 or @rlad	USPAT, US-PGPUB	2004/06/19
	18	BRS 10	((((((channel or soi or silicon or body or substrate) with capacitor) and pixel and tft) and (@ad<20031113 or @rlad	USPAT, US-PGPUB	2004/06/20
	19	BRS 16	("5745195" and (@ad<20031113 or @rlad<20031113)) and capacitor	USPAT, US-PGPUB	2004/06/20
	20	BRS 19	"5745195" and (@ad<20031113 or @rlad<20031113)	USPAT, US-PGPUB	2004/06/20
	21	BRS 16	(((((different or separate) adj (silicon or semiconductor)) with capacitor) and (@ad<20031113 or @rlad<20031113	USPAT, US-PGPUB	2004/06/20
	22	BRS 16	(((((different or separate) adj (silicon or semiconductor)) with capacitor) and (@ad<20031113 or @rlad<20031113	USPAT, US-PGPUB	2004/06/20
	23	BRS 17	(((((different or separate) adj (silicon or semiconductor)) with capacitor) and (@ad<20031113 or @rlad<20031113	USPAT, US-PGPUB	2004/06/20
	24	BRS 1	(((((different or separate) adj (silicon or semiconductor)) with capacitor) and (@ad<20031113 or @rlad<20031113	USPAT, US-PGPUB	2004/06/20
	25	BRS 4	(((((different or separate) adj (silicon or semiconductor)) with capacitor) and (@ad<20031113 or @rlad<20031113	USPAT, US-PGPUB	2004/06/21
	26	BRS 4	(((((different or separate) adj (silicon or semiconductor)) with capacitor) and (@ad<20031113 or @rlad<20031113	USPAT, US-PGPUB	2004/06/21
	27	BRS 31	(((((different or separate) near capacitor) and (@ad<20031113 or @rlad<20031113) and tft) and (substrate.clm.))	USPAT, US-PGPUB	2004/06/21
	28	BRS 2	"6031589" and (@ad<20031113 or @rlad<20031113) and tft	USPAT, US-PGPUB	2004/06/21
	29	BRS 3	((different or separate) near capacitor) and (@ad<20031113 or @rlad<20031113) and tft	EPO, JPO, DERWENT	2004/06/21
	30	BRS 4	((different or separate) near capacitor) and (@ad<20031113 or @rlad<20031113) and pixel) not (((different or se	EPO, JPO, DERWENT	2004/06/21
	31	BRS 18	(((separate near capacitor) and (@ad<20031113 or @rlad<20031113) and pixel) and (substrate.clm.)) not (((dif	USPAT, US-PGPUB	2004/06/21
	32	BRS 31	(((separate near capacitor) and (@ad<20031113 or @rlad<20031113) and pixel) and (substrate.clm.))	USPAT, US-PGPUB	2004/06/21
	33	BRS 3	(((separate near capacitor) with separate not electrode) and (@ad<20031113 or @rlad<20031113) and pixel) and	USPAT, US-PGPUB	2004/06/21
	34	BRS 8	(((separate near2 capacitor) with separate not electrode) and (@ad<20031113 or @rlad<20031113) and pixel) an	USPAT, US-PGPUB	2004/06/21
	35	BRS 1	"6031589"	USPAT, US-PGPUB	2004/06/21
	36	BRS 6	((different or separate) near capacitor) and (@ad<20031113 or @rlad<20031113) and pixel	EPO, JPO, DERWENT	2004/06/21
	37	BRS 1	"6031589"	USPAT, US-PGPUB	2004/06/21
	38	BRS 1	"6031589"	USPAT, US-PGPUB	2004/06/21
	39	BRS 0	"6031589"	USPAT, US-PGPUB	2004/06/21
	40	BRS 30	(((separate near capacitor) with separate not electrode) and (@ad<20031113 or @rlad<20031113) and pixel	USPAT, US-PGPUB	2004/06/21
	41	BRS 0	(((pixel near2 aluminum) with resistance) and (@ad<20031113 or @rlad<20031113) and (lcd)	USPAT, US-PGPUB	2004/06/21
	42	BRS 2	(((pixel near2 aluminum) with resistance) and (@ad<20031113 or @rlad<20031113)	USPAT, US-PGPUB	2004/06/21
	43	BRS 7	(((pixel near2 (low adj (resistance or impedance))) and (@ad<20031113 or @rlad<20031113) and (lcd)	USPAT, US-PGPUB	2004/06/21

FAST - [1070523.wsp.1]					File Edit Tools Window Help		11/1/02	